Review of Selected Multi-Element Resonant Topologies

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Abstract — The paper deals with an analysis and comparison of multi-element resonant topologies. The superior performance of the investigated converters provides inherent current protection and very low circulating energy. The converter consists of two resonant tanks and HF transformer in case of the LCTLC topology. The paper shows the design of the resonant elements. The converters can achieve zero current switching (ZCS) and zero voltage switching (ZVS) conditions for the primary and secondary-side of the device respectively. The converters achieve high values of power density and efficiency up to 96 % at the full load. The paper includes the basic equations, analysis and simulation of the chosen topologies (LCTLC, LCLCL, LCL2C2).

Keywords — Multi-element resonant converter, review of the resonant converters, switching conditions, LCTLC, LCLCL, LCL2C2.

I. INTRODUCTION

The progress in the development of semiconductor devices has resulted in formation of a new field of science generally called power electronics (Fig. 1). The newer and higher quality parts affect design of the resonant converter and therefore it is necessary to use sophisticated control algorithms.

In some involvement inverters with high frequency transformers the number of the semiconductor devices in converter may be reduced and this results in price of the product [1]–[3]. Increasing functionality requires more power consumption and higher density requires less size of the power supplies. Therefore, the power supplies for computing, consumer electronics and lighting applications are required to provide more power with small size and low cost.

The most effective way to achieve high power density in converters is to increase the switching frequency so that the size of the passive components, such as the capacitor and inductor, as well as the transformer can be reduced, as they occupy a large portion of the overall size [1].

II. RESONANT CONVERTERS

The group of resonant and quasi-resonant converters consists of series, parallel and series-parallel resonant circuits. By combining the basic resonant circuits modified multi-element resonant circuits arise. Resonant converters use two kinds of the switching technique: Zero voltage switching (ZVS) and Zero current switching (ZCS) [3]. Those techniques are known as soft switching. The converter can operate in ZVS and/or ZCS. The basic diagram of the resonant converter is in Fig. 2.

A. Multi-resonant Converters

The essence of the multi-resonant switch concept is to combine the desirable characteristics of both quasi-resonant switches in one device. This can be achieved using the multi-resonant switch. In this configuration of the resonant switch, resonant capacitances are placed in parallel with both the switch and the diode, resulting in desirable zero-voltage switching of both devices Zero-Voltage-Switched Multi-Resonant Converters (ZVS-MRCs) generated from PWM topologies by replacing the PWM switch by the multi-resonant switch [2]. The multi-resonant network absorbs all major parasitic components, including the transistor output capacitance, diode junction capacitance and transformer leakage inductances into the resonant circuit. This allows the multi-resonant converters to operate at high frequencies with the most favourable (zero-voltage) switching condition for all semiconductor devices [4], [5]. The base of every multi-element resonant converter is that it is usually composed of five resonant components in series or parallel circuit connection. This connection forms the resonant tank.

Using a similar concept, a family of multi-element resonant converters is proposed. Some of the five-element resonant tanks are shown in Fig. 3. Due to space limitations, the formation of all five-element resonant converters cannot be exhibited completely in this work.
III. COMPARISON OF SELECTED MULTI-ELEMENT TOPOLOGIES

As it is described in the previous chapter the multi-element resonant converter is composed of five accumulation elements. There are many possibilities of circuit connections. For this chapter three topologies were chosen: CTLC, LCLCL and LCL2C2. For the review it was done the simulation analysis provided by the MATLAB and OrCAD software.

A. LCTLC Topology

LCLCL converters (Fig. 4) are one of the novel types of converters based on the LLC resonant circuit, and LCTLC inverter consisting of the DC/DC buck converter LCLL resonant filter and HF transformer. The HF transformer can also be connected behind the LCLC filter, if necessary and it can also be used to the DC/DC boost converter types. The inverter (LCTLC) is usually used as a power supply for either HV rectifiers or HF cycloconverters or matrix converters for 2-phase motor applications respectively [6], [7].

\[
\frac{di_1}{dt} = \frac{1}{L_1} u(t) - \frac{R_2}{L_1} i_{L_1} - \frac{1}{C_1} u_{C_1} - \frac{1}{C_2} u_{C_2}
\]

\[
\frac{di_2}{dt} = \frac{1}{L_2} u_{C_2}
\]

\[
\frac{du_{C_2}}{dt} = \frac{1}{C_2} i_{L_2}
\]

\[
\frac{di_4}{dt} = \frac{1}{L_{load}} u_{C_2} - \frac{R_{load}}{L_{load}} i_{L_1}
\]

where \(i_{L1}, i_{L2}\) are currents in the inductors \(L_1\) and \(L_2\), respectively; \(i_{L4}\) is current in the load \(R_2\); \(u_{C1}, u_{C2}\) are capacitor voltages of \(C_1\) and \(C_2\), respectively, \(u(t)\) is the output voltage of the converter (filter input voltage). When the input voltage \(U_{DC}\) is varying than the RMS value of the fundamental harmonic will also vary. To be constant an asymmetric control of the duty cycle for the switches \(S_1, S_2\) has to be provided [7], [8].

![Fig. 4. Basic diagram of the LCTLC inverter.](image)

The resonant converter is composed of one series resonant tank (\(L_1, C_1\), HF transformer \((T)\) and parallel resonant tank (\(L_2, C_2\), \(R_2\)). The converter is fed by a DC source and the shape of the input voltage is switched by two switches \((S_1, S_2)\) in half-bridge connections [7]. In this case the output of the filter is the HF harmonic waveform of the voltage (and current) – direct AC output mode with THD not more than 5 %.

For the analysis, an equivalent LCTLC circuit can be created. The equivalent parameters of the HF transformer \((L_a, R_a, L_{sw}, R_{sw})\) and inter-winding capacitance \(C_{sw}\) and inter-turn capacitance \(C_a\) are included into the resulting component parameters. More about the equivalent circuit is given in the paper [7]. On the basis of it the state-space equations for the equivalent circuit with the R-L load will be [6]:

![Fig. 5. Switching waveforms of the transistor (current \(I_{D} – \text{red line}, \) voltage \(U_{DS} – \text{green line})\).](image)

![Fig. 6. Output waveforms of the LCTLC resonant converter (current \(I_ L – \text{red line, voltage } U_L – \text{green line})\).](image)

Fig. 5 shows voltage and current of the MOSFET transistor. It is also visible that the transistor is turning on in the zero voltage, the conditions preferred for the MOSFET transistors. The output voltage and current of the resonant converters is quasi harmonic. Simulation results of the LCTLC converter confirm the theoretical assumptions. Following figure shows the output waveforms of the proposed resonant converter.
The voltage transfer function shows that if switching frequency is equal to the resonant frequency the voltage gain is equal 1. Also, in this point the LCTLC converter is not depending on the load magnitude (Fig. 7).

**B. LCLCL Topology**

The proposed multi-element resonant converter is implemented and illustrated in Fig. 8. The LCLCL resonant tank is selected as the core of this converter. The half-bridge topology is adopted as the primary-side structure. It is easy to extend it to other types of input structures, including full-bridge, stacked half-bridge, and three-level structures. The centre type structure with synchronous rectifiers (SR) is chosen for use on the secondary side. Similarly, it is easy to use other types of output structures, such as the full-bridge, voltage-doubler and current-doubler structures. The secondary side with the SR is better for the low-voltage output applications. The secondary-side devices can be replaced by diodes for high-voltage applications. Thus, complicated SR driving circuits can be avoided. For the front-end converters, the SR output is applied to increase the efficiency [9].

![Fig. 8. Diagram of the LCLCL resonant converter with the SR.](image)

This topology is well known and well described in the scientific literature. The resonant circuit is composed of series-parallel LLC circuit and one parallel circuit. With one additional resonant element, a second band pass filter is created. A novel LCLCL resonant tank is proposed as an example. The basic resonant tank cell is shown in Fig. 8. The structure is similar to the previously proposed four element resonant tank, but an extra resonant inductor is inserted.

The voltage gain of the proposed LCLCL resonant tank is illustrated in Fig. 9. The curves in Fig. 9 are dependent on the load (10–100 % load).

![Fig. 9. Voltage transfer function U2/U1 of the LCLCL filter.](image)

Conceptually, $L_r$, $C_r$ and $L_p$ contribute to the first band pass filter at low frequencies. The second band pass filter consists of $L_m$, $C_r$ and $C_p$, which dominate at high frequencies. The first band pass filter can help to deliver the fundamental component to the load. It functions as the traditional resonant converters. The second band pass filter enhances the power delivery with utilization of higher harmonics. Consequently, with the injection of higher-order harmonics, the reactive power of the resonant tank can be reduced and lower RMS current and lower conduction loss can be achieved.

$$f_{01} ≈ \frac{1}{2\pi\sqrt{L_r + L_p}C_r}$$

$$f_{02} = \frac{1}{2\pi\sqrt{L_p}}$$

$$f_{03} ≈ \frac{1}{2\pi\sqrt{L_r(L_r + L_p)}}$$

$$f_{04} = \frac{1}{2\pi\sqrt{C_p(L_r + L_m + L_p)}}$$

$$Z_0 = L_r2\pi f_{01}$$

For the MOSFETs, the ZVS operation is preferred. As a result, the LCLCL resonant converter can perform boost or buck functioning, according to what is required. For the front-end converter, higher voltage gain is necessary during the holdup time. Therefore, the ZVS Region II is favorable during the holdup time operation. For overload, startup and short output conditions, the ZVS Region I is preferred to limit the current [7].

At the nominal condition, the LCLCL resonant converter operates at the resonant frequency $f_{01}$, where nearly ZV-ZCS condition can be achieved for the primary-side devices. The current of the secondary-side rectifiers naturally falls to zero, and the reverse recovery issue is eliminated [1], [4]. The topology works with 3rd harmonic as well and uses its injection to the output voltage to increase its RMS value (0.9 $U_m$) [7].

**C. LCL2C2 Topology**

In case of demand of a harmonic sinusoidal voltage for the critical load, it is necessary to use some of the resonant filter types. The resonant components are tuned to the basic harmonic. The LCL2C2 filter can be supplied by
either single-phase voltage inverter in the full- or half-bridge connection. A diagram of the resonant converter is shown in Fig. 10.

![Fig. 10. Diagram of the LCL2C2 converter.](image)

The converter in Fig. 10 can be classified as the DC/AC converter with the resonant filter. On the contrary, the following analysis is oriented on the design analysis of the LCLC components, investigation of the transfer- and transient properties and on influence of non-linearity of the inductors [10].

The state space model equations of the LCL2C2 converter are:

$$\frac{dL_{1a,b}}{dt} = -\frac{1}{L_1}i_{L1} - \frac{1}{L_1}u_{C2a} - \frac{1}{L_1}u_{C2b} + \frac{1}{L_1}u(t) \quad (11)$$

$$\frac{du_{C1a,b}}{dt} = \frac{1}{C_1}i_{L1} \quad (12)$$

$$\frac{du_{C2a}}{dt} = \frac{1}{C_{2a}}i_{L1} - \frac{1}{C_{2a}}i_{L2a} - \left(\frac{1}{r_a} + \frac{1}{R_a}\right)\frac{1}{C_{2a}}u_{C2a} \quad (13)$$

$$\frac{du_{C2b}}{dt} = \frac{1}{C_{2b}}i_{L1} - \frac{1}{C_{2b}}i_{L2b} - \left(\frac{1}{r_a} + \frac{1}{R_a}\right)\frac{1}{C_{2b}}u_{C2b} \quad (14)$$

$$\frac{di_{L2a}}{dt} = \frac{1}{L_{2a}}u_{C2a} \quad (15)$$

$$\frac{di_{L2b}}{dt} = \frac{1}{L_{2b}}u_{C2a} \quad (16)$$

Fig. 11 shows simulated waveforms of the LCL2C2 inverter (p.u.).

The experimental measurements have been done on the physical sample (Fig. 14). The resonant converter was designed to operate in the ZVS region. This confirms the switching waveforms of

![Fig. 12. Switching curves of the voltage and current of the MOSFET transistor.](image)

![Fig. 13. Output voltage and current of the LCL2C2.](image)

![Fig. 14. Experimental set-up for the LCL2C2 converter.](image)

IV. EXPERIMENTAL VERIFICATION

The output voltage and current have quasi harmonic shape (Fig. 13). The phase shift is equal 0°el. due to purely resistive load. To achieve it in reality it is almost impossible due to parasitic components. The analysis confirms the theoretical assumptions for the LCL2C2 resonant converter. Using the FFT, measured THD of the output voltage of the proposed converter was 4.589%. It is also clear that only the fundamental harmonic is at the output and a small value of the 3rd and all other harmonics are suppressed [8], [9].
the semiconductor transistor given in Fig. 15. The ZVS is in the required operation area for the MOSFET transistor in order to reduce losses.

![Fig. 15. Switching waveforms of the transistor.](image)

The waveforms in Fig. 16 are showing the output voltage and voltage on both branches with a symmetric output of the LCL2C2 circuit. The load voltage (also voltage on the branches) has harmonic shape with low THD value in both legs.

![Fig. 16. Measured symmetrical output of the LCL2C2 circuit.](image)

**Parameters:**

\[ U_{in} = 100 \text{ V}, \quad f_{in} = 100 \text{ kHz}, \quad \text{MOSFET} = 1RF5N50C, \quad L_{1b} = 66 \text{ mH}, \quad L_{2b} = 66 \text{ mH}, \quad C_{1a}, C_{2b} = 39 \text{ nF}, \quad C_{a}, C_{b} = 39 \text{ nF}, \quad \text{load: } R_{1z}, R_{2z} = 42 \text{ Ohm}, \quad 20 \text{ nH of parasitic inductance.} \]

V. CONCLUSION

In the paper it is presented the review of the multi-element resonant converters. Chapter III describes selected topologies and simulation analysis. For the simulation the MATLAB and OrCAD software was used.

The LCTLC belongs to new multi-element topologies. The connection reaches high values of the power density, efficiency up to 94 % and THD < 5 % which meets requirements for an equipment. Voltage transfer functions show that in the point where value of the switching frequency is equal to the resonant frequency the voltage gain is equal 1. Under these conditions the converter does not depend on the magnitude of the load.

The LCLCL is generally known topology. In the area of the resonant frequency No. 3 \( f_{th} \) where the voltage gain is equal to zero the converter short-circuit is proof. The topology works with 3rd harmonic and uses its injection to the output voltage to increase its RMS value (0.9 \( U_{in} \)).

The LCL2C2 is new and still not deeply investigated topology. The connection brings possibility of multifunctional output – DC, AC with HF or LF. Essences of the topology are two parallel resonant circuits which can replace center tapped transformers. Therefore, it is possible as a 2nd stage of the converter utilized half-bridge circuit connection, what decreases the number of the semiconductor devices. The LCL2C2 topology is short-circuiting proof as well. Therefore, the LCL2C2 was connected without transformer and the converter can reach efficiency up to 97 %. The THD value is above 4.5 % and power density up to 93 W/in².

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REFERENCES


